

REMARKS

Claims 4, 7, 8 and 11 have been canceled. Claims 14-25 have been added. Thus, claims 1-3, 5, 6, 9, 10 and 12-25 remain for examination.

Claims 1-4 stand rejected under 35 U.S.C. § 102(e) as anticipated by Aratani. Further, claims 7-8 stand rejected under 35 U.S.C. § 103 as unpatentable over Aratani and claims 5-6 stand rejected under 35 U.S.C. § 103 as unpatentable over Aratani in view of Han. Finally, claims 9-13 stand rejected under 35 U.S.C. § 103 as unpatentable over applicant's admitted prior art (Fig. 6) in view of Aratani and further in view of Han.

The Examiners rejections are respectfully traversed.

A first feature of the invention as recited in independent Claims 1, 2, 9, 10, 12, and 13 is that vertical image synchronizing signals are generated synchronously. Such a structure makes it possible to synchronize images displayed on a plurality of monitors.

A second feature of the invention as recited in the independent claims is that filter parameters are used for converting image data into an image format. For example, the resolution of images displayed on the monitors can be specified separately using the filter parameters.

The following features are preferable embodiments. Therefore, these features are recited in the dependent claims.

1) Filter parameters are input to image format conversion devices while no decoded image data are input to the image format conversion devices (see the period between time T1 and time T3 shown in Fig. 4).

2) The filter parameters are multiplexed with the decoded image data (see the multiplexed decoded data signal B2 shown in Fig. 4(c)).

3) The filter parameters are input to the image format conversion devices between the time the vertical image synchronizing signals are input to the image format conversion devices and the time the decoded image data are input to the image format conversion devices (see the period between time T2 and time T3 shown in Fig. 4).

4) The vertical image synchronizing signals are input to the image format conversion devices at the same phase (see the first and second vertical image synchronized signal A1 and A2 shown in Fig. 4).

5) The filter parameters are input to the image format conversion devices in synchrony with the vertical image synchronizing signals (see the first and second vertical image synchronized signal A1 and A2, and the first and second filter parameters d1 0 and d20 shown in Fig. 5).

6) The input image coded data is coded in MPEG form (see MPEG decoding portion 1 shown in Fig. 1 and first and second MPEG decoding portions 1-1 and 1-2 shown in Fig. 3).

In view of the amendments made hereto to applicant's independent claims, it is submitted that applicant's claims, as presently amended, clearly define applicant's invention over the Aratani reference taken either singularly or in combination with Han or applicant's admitted prior art.

In view of the amendments made hereto and the arguments set forth above, it is submitted that the application is now in condition for allowance and an early indication of same is earnestly solicited.

The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 19-0741. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 19-0741. If any extensions of time are needed for timely acceptance of papers submitted herewith, Applicant hereby petitions for such extension under 37 C.F.R. §1.136 and authorizes payment of any such extensions fees to Deposit Account No. 19-0741.

Respectfully submitted,

Date

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By



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